BLUETOOTH[®] CHIP DEVELOPMENT

A WEARABLE EXAMPLE

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AGENDA

- Overview Bluetooth[®]
- SmartBond[™] Bluetooth[®] LE Portfolio
- Bluetooth[®] chip development an example DA1470x





OVERVIEW

BLUETOOTH®



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The global standard for simple, secure device communication and positioning



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FUTURE ADVANCEMENTS

Bluetooth® LE Audio

■ Auracast[™] broadcast audio



Auracast[™] broadcast audio is a new Bluetooth[®] capability that will deliver life-changing audio experiences. It will let you share your audio, unmute your world, and hear your best, enhancing the way you engage with others and the world around you.

Bluetooth[®] Direction Finding

- Angle of Arrival (AoA)
 - Real Time Location Systems (RTLS)
- Angle of Departure (AoD)
 - Indoor Positioning Systems (IPS)
- Channel Sounding distance measurement









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SMARTBOND™

BLUETOOTH® LOW ENERGY



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DIALOG'S FIRST BLE SOC @ISSCC2015

- Low Cost
- Low Power / High performance
- Small size / small BOM

A 10mW Bluetooth Low-Energy Transceiver with On-Chip Matching

Jan Prummel, Michail Papamichail, Michele Ancis, John Willms, Rahul Todi, William Aartsen, Wim Kruiskamp, Johan Haanstra, Enno Opbroek, Søren Rievers, Peter Seesink, Harrie Woering, Chris Smit

Dialog Semiconductor 's-Hertogenbosch – The Netherlands



© 2015 IEEE International Solid. State Circuits. Conference

13.3: A 10mW Bluetooth Low-Energy Transceiver with On-Chip Matching

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Die Micrograph



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SMARTBOND[™] BLUETOOTH® LE PORTFOLIO



Portfolio spanning from lowest cost to highly integrated wireless application processor

- Highest integration level: PMU, security, voice, display controller, haptics and charger
- Mature Bluetooth protocol stack: proven in millions of customer products



Roadmap driving towards lowest power consumption and lowest system cost



DA1470X			

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DA1470X BLOCK DIAGRAM



- ✓ Application Cortex-M33 and 4-way 8K cache with MTB and ETM running at 32/64/96/160 MHz
- ✓ 2D GPU running at M33 speed
- ✓ SNC Cortex M0+ running at 32MHz
- ✓ CMAC Cortex M0+ running at 32MHz
- ✓ Shared system 1.5 MB retainable RAM
- ✓ Secure Octa/Quad SPI FLASH controller for XIP
- ✓ Quad-SPI PSRAM
- ✓ Voice Activity Detection with <30 uA while sleeping
- ✓ Single/Dual/Quad SPI, and Parallel interface supported for external displays
- ✓ Low I_Q DCDC SIMO Converter supplying external components
- ✓ Boost DCDC converter (4.5-5V)
- ✓ Enhanced JEITA battery charger with 720mA max charge current
- ✓ Rich set of Analog & Digital Peripheral



DA1470X WIRELESS CONNECTIVITY





POWER MANAGEMENT UNIT

- Hardware charger (up 720 mA) with programmable curves and JEITA support.
- Integrate Power path management. VSYS rail for external loads.
- Boost DCDC 4.5V-5V, 150mA
- Buck SIMO DCDC, always active
 - Quiescent Current (I_Q) <1uA
 - Typical efficiency is 80%
- Four power supply pins for external devices
- Very low Hibernation Current of around 200 nA.





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SECURE APPLICATION AND COMMUNICATION



VALUE PROPOSITION



DA1470X FAMILY AND TARGET APPLICATIONS

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DA1470X EXAMPLE APPLICATIONS



Smart Home Appliances with displays



Medical Readers



Bands and Watches

As a co-processor / Sensor Hub

Along with High End Application Processor



Bluetooth[®] Consoles (Example: E-bike, Exercise Equipment)



Mobile POS & HMI Terminals

and more.....



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HARDWARE DEVELOPMENT KIT: PRO KIT OVERVIEW







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