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QOS-ADEQUATE COMUNICATION

PAUL DETTERER

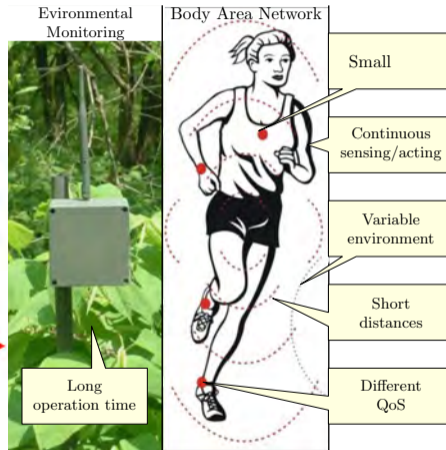
# THE OUTLINE

- Emerging applications in the Internet-of-Things
- Quality-of-Service-Adequate Wireless Receiver Design<sup>1</sup>
- Between wireless and neural networks
- Event based neural works and their implementations

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<sup>1</sup>Detterer 2023





<sup>2</sup>Navarro, Li, and Liang 2014; Jovanov et al. 2005

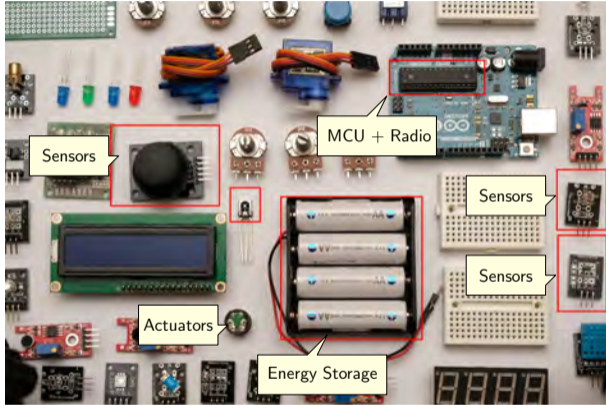


Intranet-of-Neurons

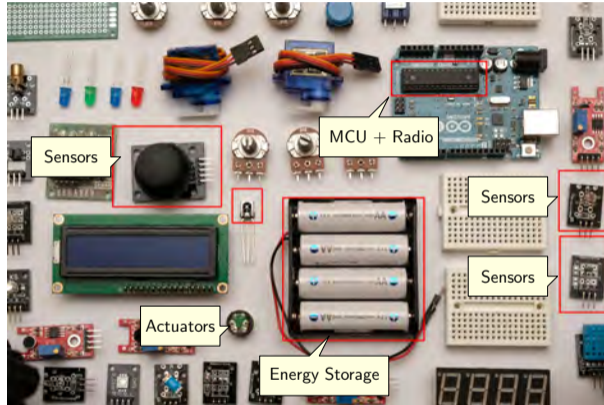


<sup>2</sup>He et al. 2022

# COMPONENTS OF IOT NODE



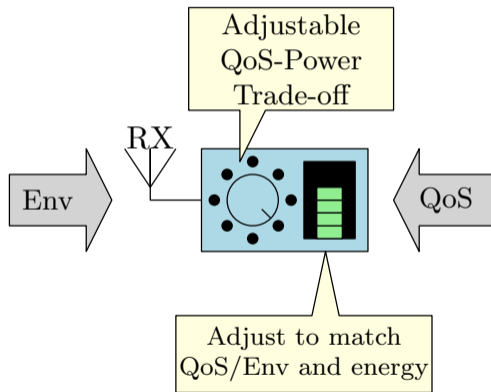
# COMPONENTS OF IOT NODE



## Observation

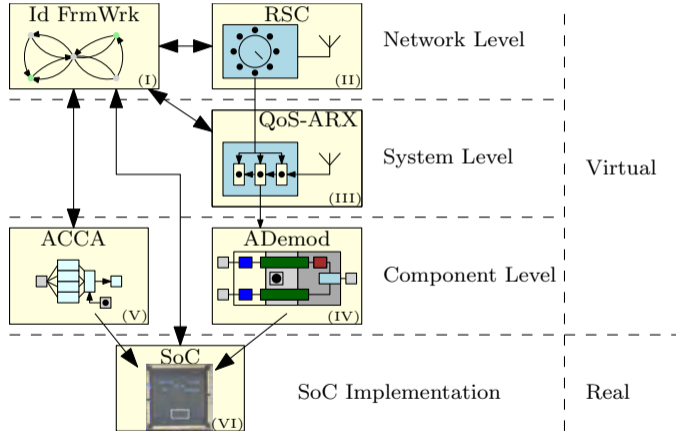
Energy efficiency is the enabler of emerging IoT applications

# QUALITY-OF-SERVICE-ADEQUATE RECEIVER (QOS-ARX)

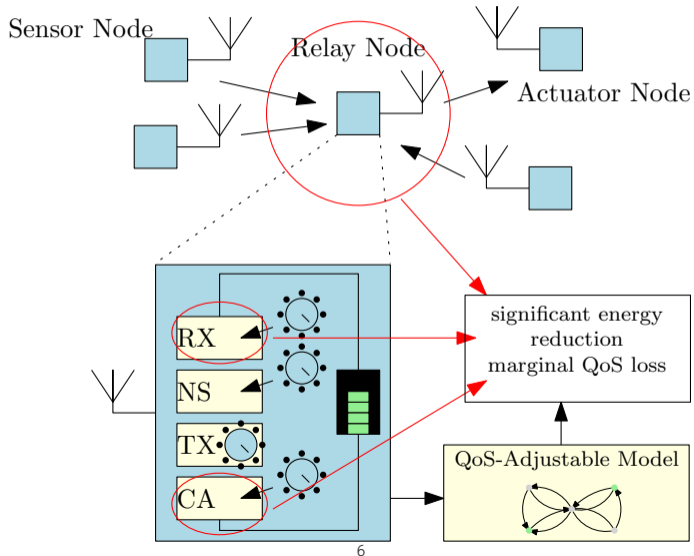




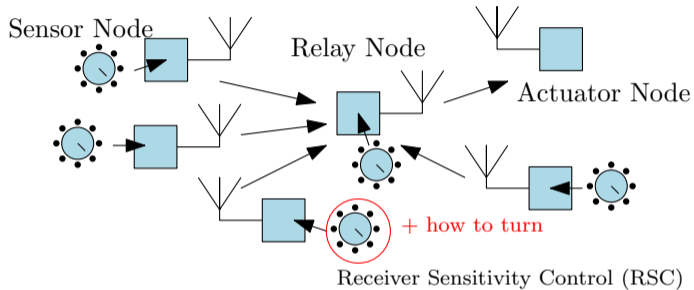
# THINKING ACROSS MULTIPLE ABSTRACTIONS



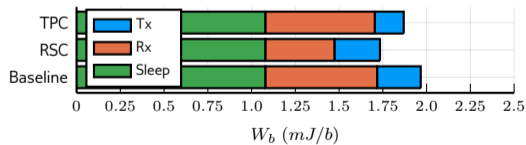
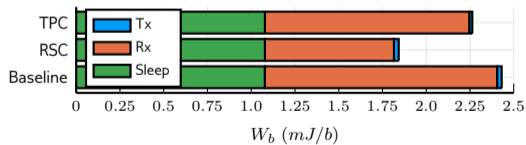
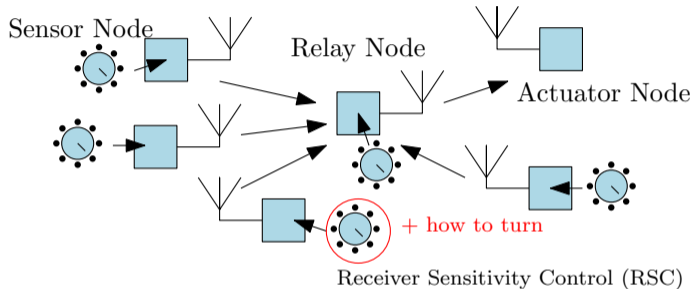
# (I) NETWORK LEVEL OPPORTUNITY IDENTIFICATION FRAMEWORK



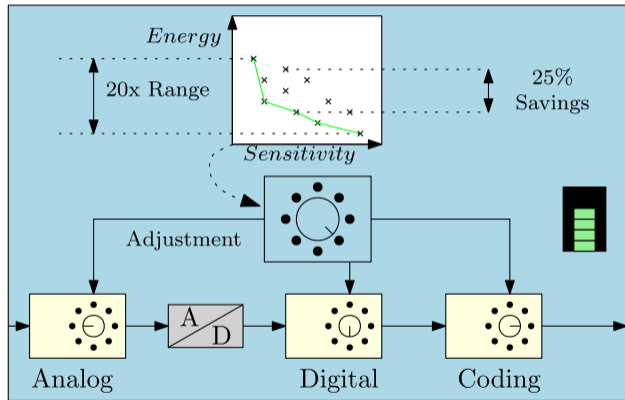
## (II) RECEIVER SENSITIVITY CONTROL



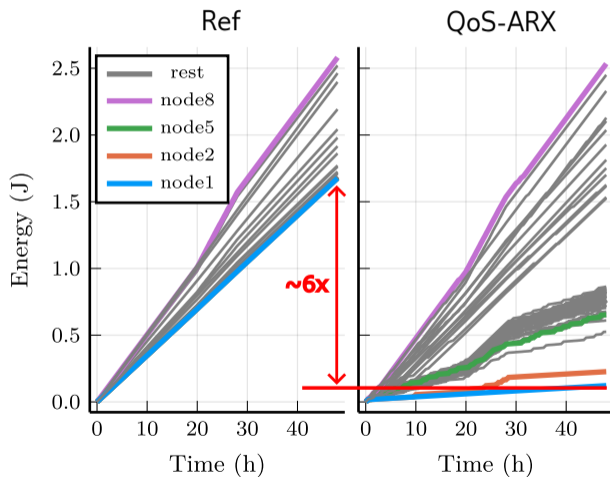
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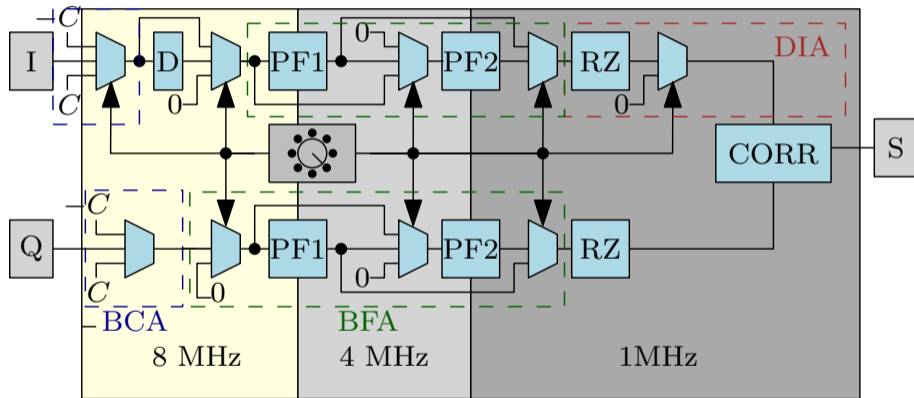
### (III) SYSTEM LEVEL QOS-ARX DESIGN



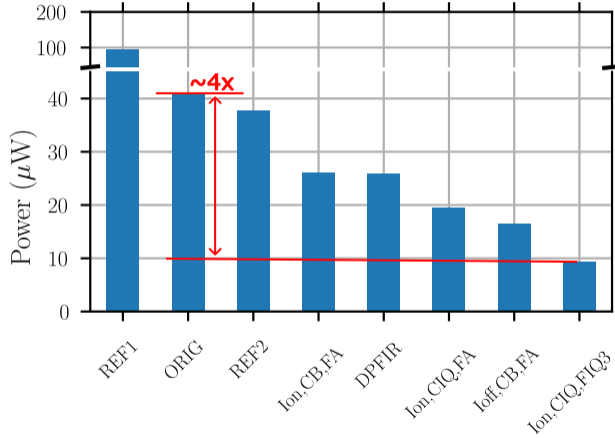
### (III) SYSTEM LEVEL QOS-ARX DESIGN



## (IV) ADJUSTABLE DEMODULATOR

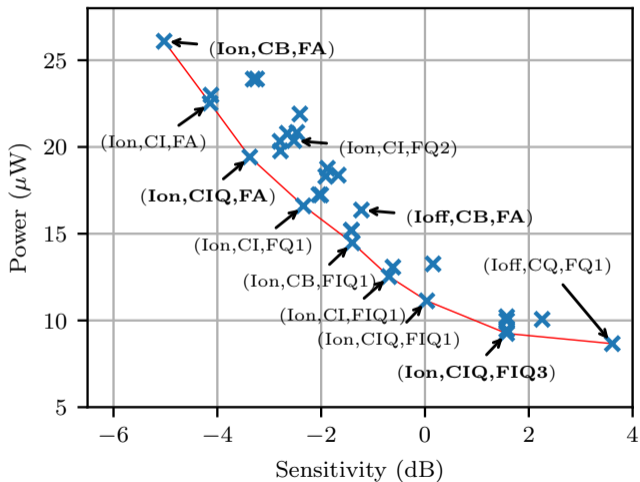


## (IV) ADJUSTABLE DEMODULATOR

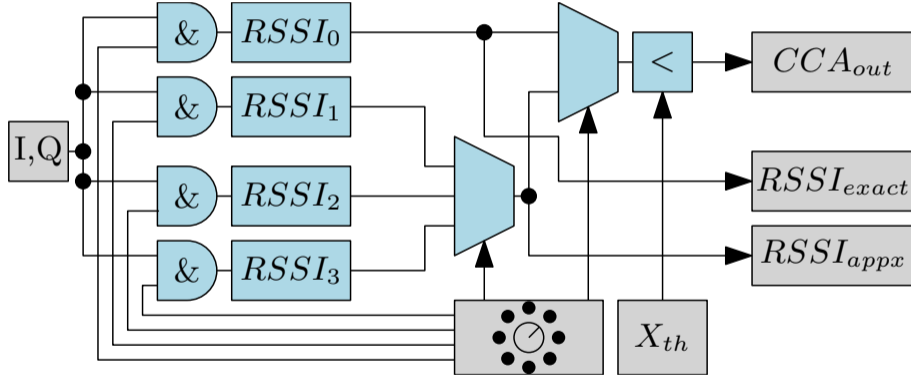




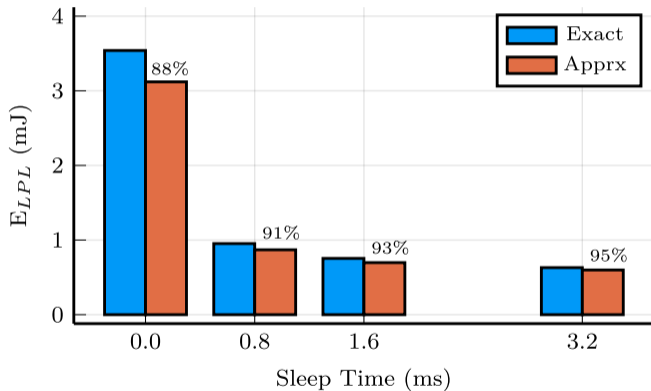
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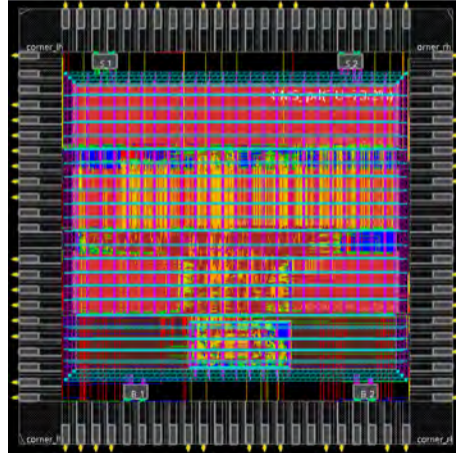
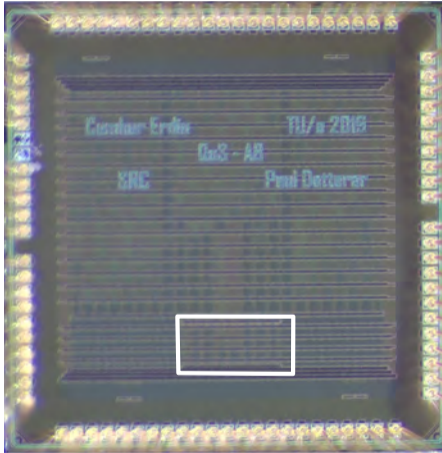
## (V) ADJUSTABLE CLEAR CHANNEL ASSESSMENT (CCA)



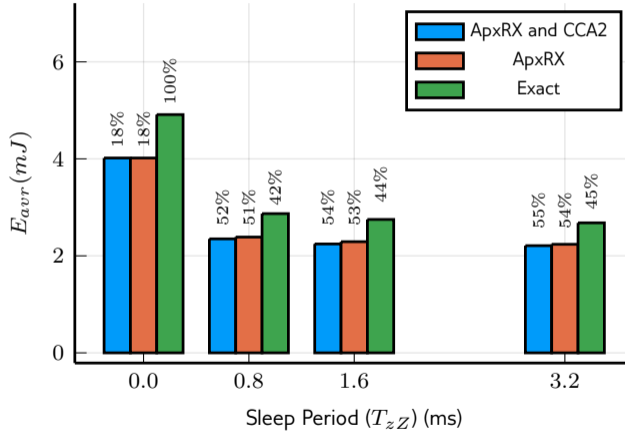
## (V) ADJUSTABLE CLEAR CHANNEL ASSESSMENT (CCA)



## (VI) DESIGN AND IMPLEMENTATION OF QOSARX SOC



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# PARADIGM SHIFTS

Trends Before	Trends Now
Flexibility ↗ → Energy ↗	

# PARADIGM SHIFTS

Trends Before	Trends Now
Flexibility ↗ → Energy ↗	Flexibility ↗ → Energy ↘

# PARADIGM SHIFTS

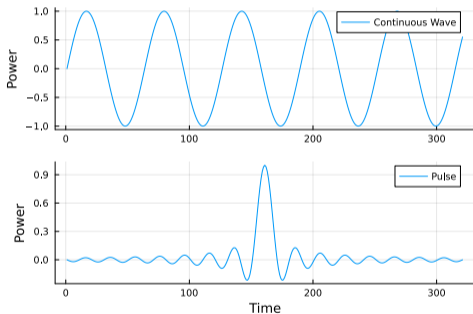
Trends Before	Trends Now
Flexibility ↗ → Energy ↗	Flexibility ↗ → Energy ↘
Broad Band ↗ → Energy ↗	



# PARADIGM SHIFTS

Trends Before	Trends Now
Flexibility ↗ → Energy ↗	Flexibility ↗ → Energy ↘
Broad Band ↗ → Energy ↗	Broad Band ↗ → Energy ↘

# PULSE BASED UWB COMMUNICATION



- Lower Power Consumption:
  - Lower Energy Consumption
- Measurable Time-of-Flight
  - Sensor
  - Secure Communication

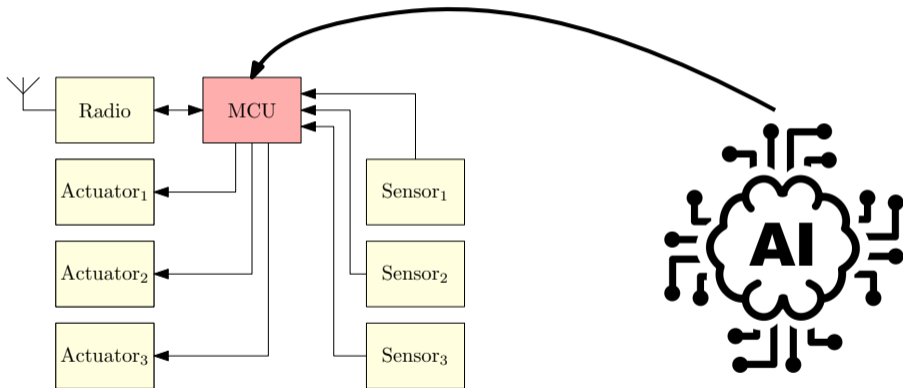
# LOW POWER PULSE BASED UWB TRANSMITTER



- $P \approx 5 \text{ mW}$
- Data rate  $\approx 27 \text{ Mb/s}$
- Signal Strength  $\approx -3 \text{ dBm}$

See more in Allebes et al. 2021.

# THE EDGE-COMPUTING



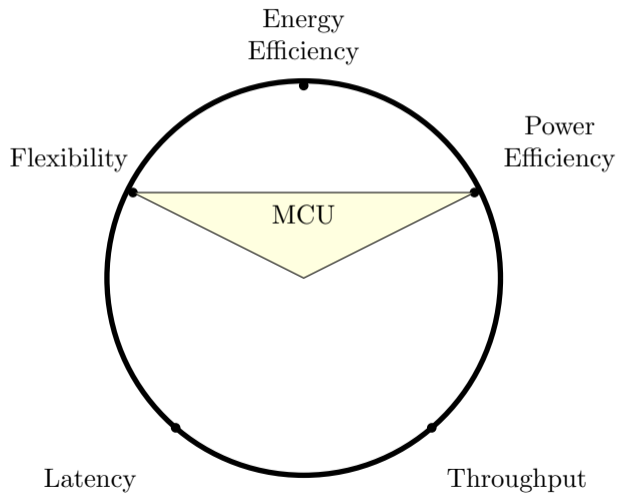
# CHALLENGES OF THE EDGE-COMPUTING

- Strict energy constraints
- Strict resource constraints

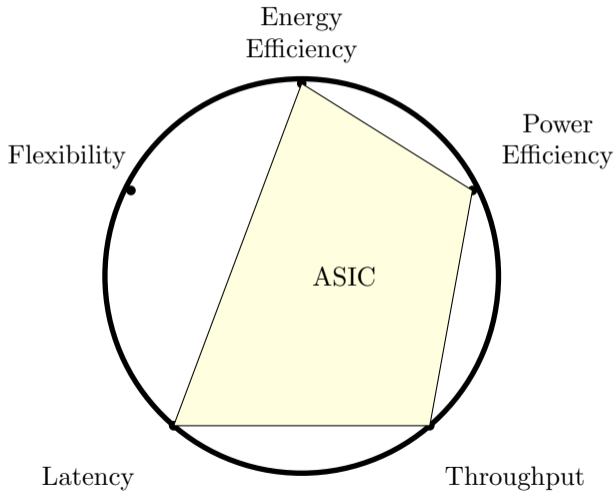
# CHALLENGES OF THE EDGE-COMPUTING

- Strict energy constraints
- Strict resource constraints
- Massively parallel algorithms

# MCU FEATURES

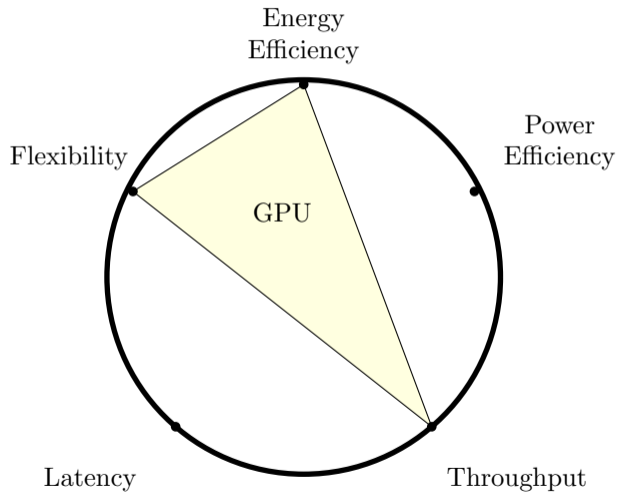


# ASIC FEATURES

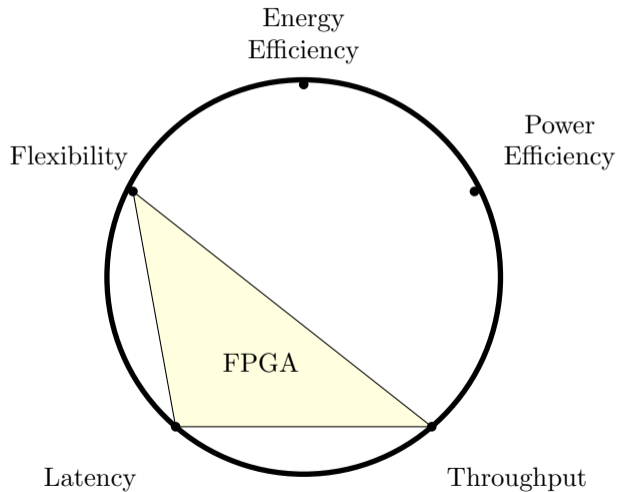




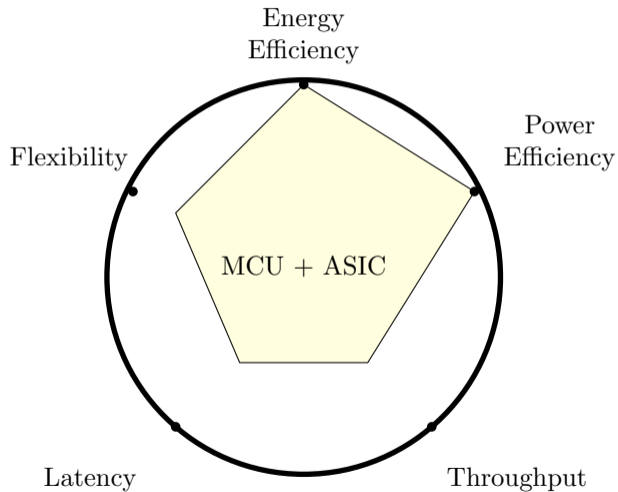
# GPU FEATURES



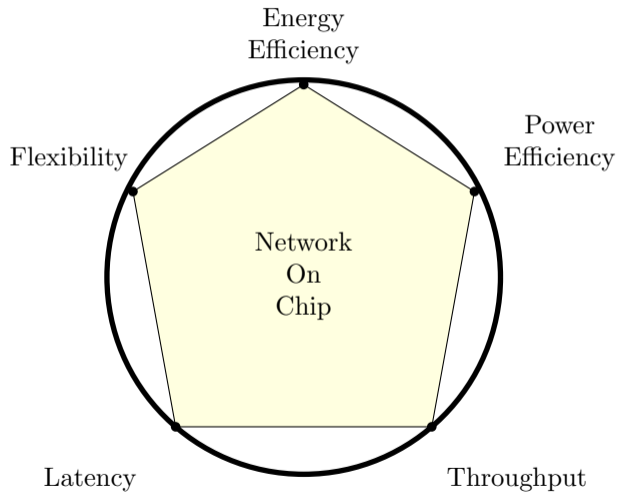
# FPGA FEATURES



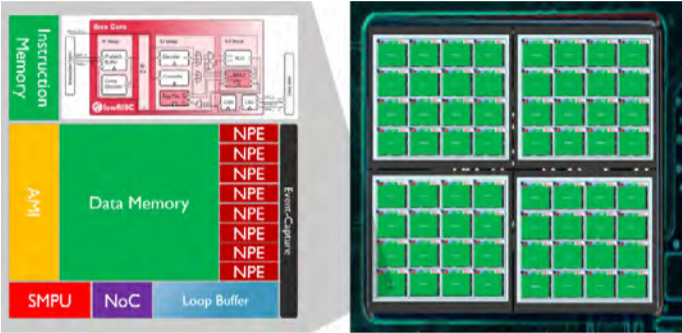
## MCU WITH ASIC



## NOC METRICS BACK TO NETWORK DESIGN









# SENECA<sup>3</sup>



## Computation vs Communication

<sup>3</sup>Yousefzadeh et al. 2022

# REFERENCES

-  Allebes, Erwin et al. (Feb. 2021). “A 3-to-10GHz 180pJ/b IEEE802.15.4z/4a IR-UWB Coherent Polar Transmitter in 28nm CMOS with Asynchronous Amplitude Pulse-Shaping and Injection-Locked Phase Modulation”. In: *2021 IEEE International Solid-State Circuits Conference (ISSCC)*.
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-  Jovanov, Emil et al. (2005). “A wireless body area network of intelligent motion sensors for computer assisted physical rehabilitation”. In: *Journal of NeuroEngineering and rehabilitation* 2.1, pp. 1–10.
-  Navarro, Miguel, Yimei Li, and Yao Liang (June 2014). “Energy profile for environmental monitoring wireless sensor networks”. In: *2014 IEEE Colombian Conference on Communications and Computing (COLCOM)*, pp. 1–6.
-  Yousefzadeh, Amirreza et al. (June 2022). “SENeCA: Scalable Energy-efficient Neuromorphic Computer Architecture”. In: *2022 IEEE 4th International Conference on Artificial Intelligence Circuits and Systems (AICAS)*. IEEE.



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